

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-36637

Haowen Bu, et al.

Art Unit: 2825

Serial No.: 10/810,905

Examiner: TBD

Filed: 03/26/04

For: CMOS Transistors and Methods of Forming Same

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited with the
U.S. Postal Service as First Class Mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz 4-7-05
Karen Vertz Date

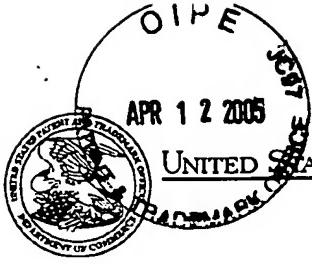
Enclosed is a copy of the Filing Receipt for United States Patent Application Serial Number 10/810,905.

Please note the change marked in red. The seventh applicant's first name should be SRINIVASAN as shown on the enclosed Declaration. Applicants respectfully request a corrected Filing Receipt. This letter authorizes any necessary charges to Deposit Account No. 20-0668 of Texas Instruments Incorporated.

Respectfully submitted,

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| APPL NO. | FILING OR 371 (c) DATE | ART UNIT | FIL FEE REC'D | ATTY.DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS |
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| 10/810,905 | 03/26/2004 | 2825 | 856 | TI-36637 | 2 | 18 | 4 |

CONFIRMATION NO. 9390

23494

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CORRECTED FILING RECEIPT



OC000000013193726

Date Mailed: 07/09/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

This application is a CIP of 10/662,850 09/15/2003

Foreign Applications

If Required, Foreign Filing License Granted: 06/07/2004

Projected Publication Date: 03/17/2005

Non-Publication Request: No

Early Publication Request: No

Title

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MAY 14 2005

PATENT DEPT

W



CMOS transistors and methods of forming same

Preliminary Class

438

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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ATTORNEY'S DOCKET NO.

TI-36637

PAGE 1 OF 2

APPLICATION FOR UNITED STATES PATENT DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification of Application Serial No. 10/810,905, filed 03/26/04; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

| | | |
|--|--|---|
| TITLE OF INVENTION: Improved CMOS Transistors and Methods of Forming Same | | |
| POWER OF ATTORNEY: I HEREBY APPOINT PRACTITIONERS AT CUSTOMER NUMBER 23494 TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH | | |
| SEND CORRESPONDENCE TO: Rose Alyssa Keagy Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265 | | DIRECT TELEPHONE CALLS TO: Rose Alyssa Keagy (972) 917-4167 |
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| COUNTRY OF CITIZENSHIP: United States | COUNTRY OF CITIZENSHIP: United States | COUNTRY OF CITIZENSHIP: India |
| SIGNATURE OF INVENTOR: | SIGNATURE OF INVENTOR: | SIGNATURE OF INVENTOR: |
| DATE: | DATE: | DATE: |



ATTORNEY'S DOCKET NO.
TI-36637

**APPLICATION FOR UNITED STATES PATENT
DECLARATION AND POWER OF ATTORNEY**

| | | |
|---|---|---|
| TITLE OF INVENTION: | | |
| Improved CMOS Transistors and Methods of Forming Same | | |
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| DATE: | DATE: | DATE: |
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| DATE: <i>x 04/05/2005</i> | DATE: | DATE: |